

**AMENDMENTS TO THE SPECIFICATION**

**Please insert the following on page 5 at line 30 of the specification before the heading “DETAILED DESCRIPTION OF INVENTION”:**

**BRIEF DESCRIPTION OF THE FIGURES**

Fig. 1 is a schematic view showing the constitution according to one embodiment of the jig for producing capacitors of the present invention.

Fig. 2 is a schematic view showing the constitution according to another embodiment of the jig for producing capacitors of the present invention.

Fig. 3 is a schematic view showing the constitution of the back surface according to one embodiment of the jig for producing capacitors of the present invention.

Fig. 4 is a schematic view showing the constitution according to another embodiment of the jig for producing capacitors of the present invention.

Fig. 5 is a schematic view showing the constitution of the back surface according to one embodiment of the jig for producing capacitors of the present invention.

**Please delete the heading “BRIEF DESCRIPTION OF DRAWINGS” on page 29, line 6 of the specification.**

**Please delete the 2<sup>nd</sup>-5<sup>th</sup> full paragraphs on page 29, lines 7-21 of the specification.**

**Please replace the paragraph bridging pages 13-14 of the specification with the following amended paragraph:**

Fig. 2 is a schematic view showing one example of a jig for producing capacitors, comprising a current regulating diode group connected by wiring to a part of a plate-like jig for producing capacitors. On an insulating substrate 2, two or more electronic members 6 obtained by connecting in series the connection terminal 4 for electric conductor to a cable terminal 5 are arranged in the same direction. Each of the cathodes 1a of the current regulating diodes 1 is connected by wiring via a cable 10 to each cable terminal 5 and the anodes of the current regulating diode group are connected by circuit to a terminal 3. The jig having a constitution of Fig. 2 is used in practice by adjusting the dimension of the electric conductor (not shown) having formed on the surface thereof a dielectric layer and then connecting it to each connection terminal 4.